

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	09/173,129	PARK ET AL.	
	Examiner	Art Unit	Page 1 of 1
	Ginette Peralta	2814	

U.S. PATENT DOCUMENTS

*		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
							APS	OTHER
<input type="checkbox"/>	A	5,352,620	Oct. 1994	Komori et al.	437	52	<input checked="" type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	B	4,505,028	Mar. 1985	Kobayashi et al.	29	578	<input checked="" type="checkbox"/>	<input type="checkbox"/>
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NON-PATENT DOCUMENTS

*		DOCUMENT (Including Author, Title Date, Source, and Pertinent Pages)	DOCUMENT SOURCE **	
			APS	OTHER
<input type="checkbox"/>	U	S. Wolf. Silicon Processing for the VLSI Era, Volume 2: Process Integration. Lattice Press, California, 1990. Page 598.	<input type="checkbox"/>	<input checked="" type="checkbox"/>
<input type="checkbox"/>	V	S. Wolf, R. Tauber. Silicon Processing for the VLSI Era, Volume 1: Process Technology. Lattice Press, California, 1986. Page 198.	<input type="checkbox"/>	<input checked="" type="checkbox"/>
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PTO-892 (Rev. 03-98)

Notice of References Cited

Part of Paper No. 2

Notice of References Cited	Application/Control No. 09/173,129		Applicant(s)/Patent Under Reexamination PARK ET AL.	
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